

PATENT NUMBER

U.S. UTILITY Patent Application

m.H. O.I.P.E.
 SCANNED 10/10/2014 O.A. 10/10/2014

PATENT DATE

APPLICATION NO. 09/823679

CONT/PRIOR

CLASS

SUBCLASS

ART UNIT

EXAMINER

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APPLICANTS

TITLE

System for improving the parasitic resistance and capacitance effect of a transistor-based switch

PTO-2040
12/99

ISSUING CLASSIFICATION

[illegible]

TERMINAL DISCLAIMER

☐ The term of this patent subsequent to _____ (date) has been disclaimed.

☒ The term of this patent shall not extend beyond the expiration date of U.S. Patent No. 5,711,711.

☐ The terminal ____ months of this patent have been disclaimed.

DRAWINGS

Sheets Drwg.

Figs. Drwg.

Print File

(Assistant Examiner)

(Date)

(Primary Examiner)

(Date)

(Legal Instruments Examiner)

(Date)

CLAIMS ALLOWED

Total Claims

Print Claim for O.G.

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

ISSUE BATCH NUMBER

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